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(54) Title: INFRARED CAMERA WITH THERMOELECTRIC TEMPERATURE STABILIZATION (57) Abstract A camera system primarily for infrared radiation having a focal plane array of microbolometer elements in a vacuum package with inexpensive thermoelectric temperature stabilization is shown. The stabilization temperature may be selected by a designer or a user over a wide range of temperature, but room temperature use is primarily expected. The microbolometers are passive elements and the readout scheme involves a sweep of the array with a short duration pulse-high level bias current. 		

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INFRARED CAMERA WITH THERMOELECTRIC TEMPERATURE STABILIZATION

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FIELD OF THE INVENTION

This invention relates to the art of video imaging and has particular applicability to video cameras capable of producing visual images from infrared radiation and even more particularly to providing such images at relatively low cost.

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BACKGROUND OF THE INVENTION

This invention applies to a new type of radiation-sensitive focal plane operating near room temperature: a two-dimensional array of microminiature bolometers (microbolometers). This invention describes 1) an apparatus and method of using such focal planes in an IR imaging camera without the necessity to use a chopper; 2) an apparatus and method for efficiently retrieving the information contained in the focal plane; and 3) a camera system in which these focal planes can operate to produce video output signals.

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SUMMARY OF THE INVENTION

The camera system described in this invention has a radiation receiving system including preferably a lens, reflective or other optical system assembly with an iris to focus the scene on a focal plane array contained within a vacuum chamber. The light receiving surface or "window" of the vacuum chamber is invisible to the wavelengths of radiation desired to be recorded or received by the focal plane array.

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The vacuum package contains the focal plane array affixed to one surface of a thermoelectric temperature stabilizer. On the thermoelectric temperature stabilizer or the substrate of the focal plane array is a temperature sensing device. This whole assembly is then fixed into the base and supporting structure of the vacuum chamber. Electrical connections are made through the wall of the vacuum chamber to the various components within it and this allows for a bias current to be pulsed to specific focal plane array elements at specific instants in time so that they will produce an output related to the radiation received thereby. Control leads for the thermoelectric temperature stabilizer also pass through the vacuum chamber and allow for it to be temperature stabilized based on the temperature sensed by the temperature sensor. A high stability bias source provides voltage at a constant level to be sent via a decoder to the particular focal plane array elements to be sensed in a sweep. The output of

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each element is received by a preamplifier, the output of which is translated into a digital signal and forwarded to an image processor which produces a video output.

5 In the preferred embodiment, the window to the vacuum chamber through which the radiation passes is transparent only to infrared and the focal plane array consists of microbolometer elements appropriately spaced upon the array to dissipate heat similarly to the microbolometer focal plane arrays described in U.S. Patent Serial Nos. 06/511,369, 06/511,370, 07/035,118, 07/172,110 and 06/887,495.

BRIEF DESCRIPTION OF THE DRAWINGS

10 Figure 1 is a three-dimensional block diagram of the major components of the preferred embodiment camera.

Figure 2 is an exploded view of the vacuum package.

Figure 3 is a side view of the vacuum package.

15 Figure 4 is an electrical block diagram of a small heuristic focal plane array in accord with one embodiment of the invention.

Figure 5 is an electrical wiring diagram of a small section of the drive circuitry associated with the preferred embodiment focal plane array.

20 Figure 6 is a graph of time versus temperature and voltage illustrating the effect of the application of pulse bias voltage to the passive elements of the focal plane array over time.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The Camera

25 Referring first to Fig. 1, the layout of the camera 100 is shown. Light or radiation received by the lens or reflective optical system 71 enters through the iris 72 to the surface of the window on the package 10 or vacuum chamber 10 containing the focal plane array (not shown). Small dotted lines c indicate that many pins connect the package 10 to the rest of the camera electrically.

30 The temperature of the focal plane array is kept constant by a thermoelectric controller 73 which sets the temperature based on the output of a temperature sensor inside the package 10. The temperature at which the array is kept is referred to as a stabilization temperature. For the microbolometers we used, the range at which this has been tested is from zero to 30° C, although there is no reason much higher or lower stabilization temperatures could not be used.

35 A line t indicates that temperature information is received by the thermoelectric controller. If the temperature information is what is desired, no signals will be sent over the other lines t_a and t_b . However, in the preferred embodiment the thermoelectric controller will be controlled by sending power over

either line t_a or t_b depending on the direction (warmer or cooler) of the desired change in temperature.

5 The passive elements of the focal plane array in the package 10 need to be polled or interrogated by providing a voltage or a current. Accordingly, an array bias is produced by an array bias block 76, this electrical unit being a highly stable (low noise, low drift) voltage supply in the preferred embodiment. The inventors have had some success using a simple battery, although certainly other sources well known to those in the art could be employed. The voltage must be provided to the passive elements in the array in some sequence in the preferred embodiment. Accordingly, a
10 set of decoders 75 is used which is controlled by a logic controller 77. The logic controller 77 will steer the current by controlling the decoders (essentially operating as a large multiplexer) in a sweeping sequence such that each row by column address is polled over a sweep one time before being polled again in the second sweep. The decoders then steer the bias current provided by the array bias 76 to the appropriate
15 pins c attached to the package 10.

Output pins c are all connected to a set of preamplifiers illustrated here as block 74. The output of these preamplifiers may be taken either serially or in a buffered form by an analog to digital conversion module 78 which can then provide a digital indication of the value of the analog signal level input thereto for each output
20 from each preamplifier. The electrical characteristics of the preamplifier are designed to be stable with time and temperature, or correction for such changes are applied in the image processor. The inventors have had some success with the latter technique, employing calibration signals injected into the preamplifiers. Alternatively, this may be attained by temperature- and time- independent circuit designs well known to those
25 in the art.

In this camera, is also provided an image processor 80 to which a driver's console 79 is in electrical and controlling contact. In other words, by controlling how the digital values are received which are already in the image processor, an operator can change the sensitivity, receive freeze-frame information and variously correct or
30 change the video signal which will be output by the image processor 80. Image processors are fairly well known in the art of video cameras and generally use digital signal processing circuits and various adjustments to the incoming input signal to produce an output video signal here illustrated on line 82.

Software control is the preferred embodiment since the algorithms normally
35 used are most conveniently implemented by software, rather than by hard-wired logic. In this way we can implement things which are difficult to implement in hardware, because different algorithms are required at different instants in time, and because

signals from different areas of the array may be treated with different processing algorithms.

Some advantages are inherent in this camera design which are not readily apparent.

5 The iris may be closed momentarily (e.g., after camera manufacture, or at camera start-up) to allow the image processor 80 to average several image frames and store this digital data in a long-lived digital memory (which may be in the image processor systems). A simple expedient of a lens cap or shutter may be employed instead, if desired. During normal camera operation the iris 72 remains permanently
10 open, or partially closed if it is desired to reduce the radiation intensity falling on the focal plane. The image processor subtracts the incoming signals from the digital data in its long-lived memory on a pixel-by-pixel basis. This provides offset correction for each pixel in the image to be viewed by a human observer, a requirement and process well known to those in the art. Provided the focal plane remains maintained at a
15 stable temperature by the TE stabilizer, the preamplifier electrical characteristics do not change, and no change is made to the array electrical polling, no chopper is required to interrupt the radiation from the scene. The absence of a chopper produces many desirable benefits: a cheaper and more reliable camera, a lower electronic speed requirement, and elimination of the sensitivity loss produced by the periodic radiation
20 obstruction of a chopper.

The Package for the Array

Referring to Fig. 2, the vacuum package 10 is shown in an exploded view. The package consists of a base plate 11 containing apertures 12, 13, 14, and 15(not shown) for connecting it to the camera, a surrounding wall structure 16. having an
25 internal step 17, on which are located pads 18. Wires 19, of which only a few are shown, are connected to these pads. Basically, this exploded view and its limited elements is shown for illustrative purposes only and not to be taken in a limiting way.

The wall 16 surrounds a bounded area 54 within which there is an aperture 53 in the upper surface of base plate 11. The aperture extends into a tube 50 whose
30 inner space is defined by dotted lines 52 and which in the final preferred embodiment product is crimped off at 51. A getter 55 may also be used. A thermoelectric temperature stabilizer 20 fits into the bounded space 54 in the finished package. The thermoelectric temperature stabilizer consists generally (in the preferred embodiment) of an upper plate 20a and a lower plate 20b of what is usually beryllium oxide
35 sandwiching a layer of other materials such as bismuth and/or antimony, or other suitable materials known in the art. Power leads "-" and "+" provide power to either one of the plates thus either cooling or warming the device. The surfaces such as upper surface 26 are metallized for bonding preferably by solder to either the upper

surface of substrate 11 on the one side or the lower surface of the focal plane array to surface 26. A temperature sensing device 27 in the preferred embodiment, for simplicity's sake a thermistor, is also attached to the surface 26 of the thermoelectric temperature stabilizer 20. Numerous temperature sensors are currently under
5 development or readily available and, depending on the economics, may also be employed. If the temperature sensor is small enough it could even reside on the focal plane array chip itself. This would be left up to the needs and desires of the designer. All that is necessary is that a very accurate reading of the temperature of the focal plane array be given by the temperature sensing device.

10 Also, the inventors have had some success using temperature sensors fabricated on the focal plane array chips. These sensors are periodically polled by the focal plane readout electronics in the same way as the microbolometers, and the temperature data is sent to the image processor in the same way as the microbolometer signals. The inventors have had some success in using the image
15 processor to use these temperature signals to improve the image quality by correcting for small temperature drifts in the cameras. These could be microbolometers which are intentionally made unresponsive to infrared radiation.

The focal plane array chip 30 has bonding pads or leads 31 preferably around its edges. The focal plane array elements which are sensitive to radiation are in area
20 33 in the preferred embodiment chip. If a temperature sensor were employed on the focal plane array, it would most likely be preferred to be included in area 33.

Topping off the package is a window 40 which is transparent to the type of radiation expected and desired to be received by the focal plane array 33. The lower
25 peripheral edges of this window should be metallized in the preferred embodiment to permit soldering. For the most desired embodiment at the current time, the focal plane array elements are passive microbolometer elements coated with vanadium oxide which produce a change in resistivity based on the amount of infrared radiation received by each element in a way related to that disclosed in U.S. Patent Application
30 Serial No. 07/035,118. In this infrared sensitive preferred embodiment a germanium window that is anti reflective is employed for window 40. In the currently preferred embodiment package, the base 11 and its surrounding wall 16 is a custom integrated circuit package produced by Kyocera of Japan made of aluminum oxide (Al_2O_3) for the wall with a base plate of a copper alloy. The preferred embodiment tube is copper and the getter is a metallic alloy well known to those in the art, activated far
35 from the device after the tube is used for pumping out the air within the sealed package 10. The getter is then pushed up close toward the package in the tube 50 and the package is sealed off by crimping the tube at 51. In the preferred embodiment solder is used for everything but electrical connections which are bonded. However,

as technology and practice improves, attaching the electrical leads and the components together may be done in different ways within the scope of this invention. Likewise, any materials which would readily substitute for those described hereabove may also be substituted without going beyond the scope of this invention.

5 Referring now to Fig. 3, a side view of the package 10 is shown having the copper tube 50 bonded to the base plate 11 with the getter 55 inside. The electrical wires, w , w_t , w_{t_b} , and w_{t_a} , provide power and readout to and from the three devices in the vacuum space 53. The leads or pads on step 17 from these wires are connected through wall 16 to wires 19 which may then be bonded to connectors to external
10 elements such as the preamplifiers in block 74 of Fig. 1, the thermoelectric controller of block 73 of Fig. 1, and the decoders supplying the bias voltage through block 75 of Fig. 1. Radiation which reaches the window 40 in the right wavelength (R_1) will go through the window. Radiation which is not of the right wavelength (R_2) will be reflected off of the window 40 or absorbed by it. As stated before, the various
15 surfaces are held together in the preferred embodiment by soldering such that joints 61, 62, 63, and 64 would be solder connections. As also described before, these connections may be made by other means but presently soldering is preferred.

With reference to the thermoelectric temperature stabilizer 20, these devices are currently available from several manufacturers, the preferred source currently
20 being Marlow Industries of Dallas, Texas. For an infrared sensing focal plane array having 80,000 elements, a preferred thermoelectric cooling device would be Marlow Industries model number SP5030-03-BC.

Readout From Passive Elements

Referring now to Fig. 4, in which an abbreviated schematic diagram of a
25 focal plane array 33A is heuristically drawn having a number of inputs (here four, but a given number could be m) and a number of outputs (here three, but could be n). The input lines provide a bias current preferably one input line at a time which, using this illustration, would drive the entire row of pixels such as pixel P at address 2,3 also known here as 2, n , to provide an output indicative of their state on the output
30 lines 1- n . In this simplified diagram, each one of the pixels also called elements, units, microbolometers, or bolometers, receive the pulsed current from input line 1- m through a diode d , the one labeled in this diagram being $d_{(m1)}$.

A preferred implementation is really a bipolar transistor version of Fig. 4 (attached) because it requires less current to be drawn by the row. Circuit operation
35 is otherwise the same for diodes and transistors. It is also possible to use field effect transistors with very little else changed. Conceptually, the diodes could be considered switches of any kind.

The principle of operation can perhaps be best described with reference to Fig. 6. In the case of the passive pixel whose electrical characteristics are modified by receiving radiation or by changes in their temperature due to receiving such radiation, the graph of temperature and voltage versus time is well suited for illustration purposes. In the most preferred embodiment of course, the pixels or microbolometers and the window is transparent to infrared radiation. The microbolometers change temperature due to the receipt of radiation through the window and the amount of resistance through the microbolometer surface structure, a resistor, is reduced as heat in the microbolometer assembly is increased. The vanadium oxide material we currently prefer (a semiconductor) decreases in resistance as temperature rises. There are other materials (e.g., metals) whose resistance changes in the opposite sense.

In Fig. 6 the voltage level indicated by line 5 is that of the pulse biased current supplied to a single microbolometer in a focal plane array over time. In an 80,000 pixel array the pulse width is approximately 5 to 6 microseconds and based on the preferred addressing scheme addressing 14 pixels at a time. The temperature curve 6 shows that a single microbolometer temperature can be raised roughly 2° C each time the roughly 200 to 300 microamp current pulses them. The 22° Centigrade line is indicated to show the preferred temperature for the focal plane array. Note that the temperature of an individual pixel floats just above the 22° centigrade mark at all times when not pulsed with current. It should be recognized that over and above the temperature variation caused by the bias current pulses illustrated in Fig. 6, incoming radiation from the scene causes an additional temperature variation.

The 22° Centigrade is considered the stabilization temperature for the preferred embodiment focal plane array. At this temperature, a tenth degree temperature change in the microbolometer will produce a perceivable change in resistance, about 0.2%.

Because of the short duration of the time in which to read out signals in an array to produce a moving video image recognizable as a real time image by human beings, high band width amplifiers are used in the preamp 74 of Fig. 1. The large current tends to compensate for the inherent noisiness of these high band width amplifiers. The large bias current that can be safely used with pulsed bias improves the sensitivity of the microbolometers proportionately, tending to compensate for the inherent noisiness of these high-bandwidth amplifiers, and allowing sensitive IR imaging with bolometer arrays.

Pulse biased current schemes have been used to read out information in passive devices previously such as in U.S. Patent No. 3,900,716 for memory chips. Nevertheless this general scheme has not been applied to focal plane array

technologies. Nor even in the earlier instance has it been applied in the way it is done here.

Note that in this invention, because the bias current is applied in short pulses, high bias currents can be used which would damage the pixels if applied continuously. The sensitivity of the microbolometers is higher with pulsed bias current because the sensitivity improves approximately in proportion to the bias current level.

Refer now to Fig. 5 showing a slightly more detailed wiring schematic diagram of a portion of a focal plane array of passive pixels. The only passive pixel illustrated here is the one labeled RP connected between column 91 and row 95 by transistor QP.

Bolometers RP and pixel transistors QP are situated at the intersection of each row and column (one of each of which resides at each row/column intersection but only one of which is shown here). Each row is controlled by transistors $QR_{(1-4)}$ and resistors $RR_{(1-4)}$. Each column is controlled by transistors $QC_{(1-4)}$ and $QC2_{(1-4)}$. Rows are grouped into row-groups with several rows in each row-group (two shown). Columns are grouped into column-groups with several columns in each column-group (two shown). This grouping arrangement allows a large array to be controlled by relatively few control lines (row-group-select, Row-select, column-group-select). Several signal lines S1, S2 shown) carry signals to several preamplifier transistors and resistors (QAMP1, QAMP2, RC1, RC2 shown), providing amplified output signals (OUT1, OUT2 shown).

In operation, one row is biased to an "ON" potential by application of control signals to row-select and row-group-select control lines, and all other rows are biased "OFF" by the RR resistors. Simultaneously, control signals are applied to column-group-select lines so as to read out signals from several (two shown) microbolometers RP in that chosen row simultaneously. The readout signal consists of the current flowing into the columns of the selected column-group. The readout signal currents are converted to amplified voltage signals by preamplifier transistors (two shown). Control signals are applied to column-group-select lines until all microbolometers in the chosen row have been read out. Another row is then chosen to be biased "ON", and the above process repeated. This is continued until all microbolometers in desired rows and columns have been read out.

In this mode of operation, the bias current flowing through the bolometers is in the form of short pulses, and the temperature of the bolometers also varies in a pulsed manner. This pulsed bias operation allows higher bias currents to be applied than would be allowed with continuous bias currents (continuous bias current would have to be kept much smaller to prevent destruction of pixels or bolometers by overheating), and produces a correspondingly higher sensitivity to infrared radiation.

The simultaneous readout of several pixels allows the current pulse duration to be selected to lie at an acceptable value for optimum array operation.

The grouping of the rows and columns into row-groups and column-groups allows a large array to be controlled with relatively few control lines.

5 VSUB is a bias potential applied to the circuit of Fig. 5. Its purpose is to maintain the transistors biased for proper operation, and to provide a "sink" for the pulsed bias currents. Its name arises from the fact that this connection happens to be the substrate of the silicon chip. VROW is applied to resistors RR to enable these resistors to bias unused rows "OFF".

What is claimed is:

1. An infrared camera having:

5 optical assembly (71) which brings light/infrared radiation to a focal plane array (33) of infrared sensitive microbolometers on a semiconductor substrate (30) that is maintained constantly at any selectable temperature, over a wide range, by a thermoelectric temperature stabilizer (20) wherein said thermoelectric temperature stabilizer is regulated by a controller (73) which maintains temperature based on a signal received from a temperature sensor (27) in close thermal association with said array.

2. An infrared camera as set forth in claim 1;

15 wherein said focal plane array is swept to produce output signals by a decoder (75) which selects particular column and row addresses in said array by providing a bias current thereto in short duration pulses from a bias current source (76), said selecting of row and column addresses being determined by a logic controller (77) which generates signals setting switches in said decoder in a pattern designed to sweep the entire array at an appropriate rate.

3. An infrared camera as set forth in claim 2;

25 wherein the level of bias current pulsed to each pixel is substantially greater than what would be a safe level for each pixel if said pulse were for a long duration.

4. An infrared camera as set forth in claim 2 having substantially serial output of analog signal values returned from said sweep of said focal plane array.

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5. An infrared camera as set forth in claim 2, wherein said substantially serial output is produced after receipt of substantially individual analog outputs from said row and column serial sweep by individual preamplifiers for each pixel address, wherein the output of said preamplifiers (74) is received by an analog/digital module (78) which converts each analog value to a digital representation of said analog value and then transfers the converted values to an image processing unit which converts the values received to a standard video output signal.

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6. An infrared camera as set forth in claim 1 wherein the stabilization temperature approximates room temperature.
7. An infrared camera as set forth in claim 1 wherein said camera has no chopper.
8. An infrared camera as set forth in claim 1 wherein the thermoelectric stabilizer maintains the focal plane array temperature so constant that no chopper is required.
9. An apparatus for maintaining a semiconductor device at relatively constant temperature in a vacuum comprising:
a substrate having first and second surfaces with an aperture formed therethrough,
said aperture being surrounded along with an area of said first surface by a contiguous wall structure, said area inside said wall being a bounded surface area,
a thermoelectric temperature stabilizer mounted firmly to said bounded surface so as to allow for fluid communication through said aperture, and having an opposite surface,
said semiconductor device being mounted to the opposite surface of said thermoelectric temperature stabilizer,
a temperature sensor mounted to said semiconductor device in thermal proximity to said sensor array,
a window for admitting some radiation to said semiconductor device mounted to seal in said thermoelectric temperature stabilizer, temperature sensor and semiconductor device in the interior space definable by said first surface, said window for admitting some radiation and said wall structure,
10. An apparatus for maintaining a sensor array at relatively constant temperature in a vacuum comprising:
a substrate having first and second surfaces with an aperture formed therethrough,
said aperture being surrounded along with an area of said first surface by a contiguous wall structure, said area inside said wall being a bounded surface area,

- a thermoelectric temperature stabilizer mounted firmly to said bounded surface so as to allow for fluid communication through said aperture, and having an opposite surface,
said sensor array being mounted to the opposite surface of said thermoelectric temperature stabilizer,
5 a temperature sensor mounted to said sensor array in thermal proximity to said sensor array,
a window for admitting some radiation to said sensor array mounted to seal in said thermoelectric temperature stabilizer, temperature sensor and sensor array in the interior space definable by said first surface, said window for admitting some radiation and said wall structure,
- 10
11. An apparatus for maintaining an infrared sensitive semiconductor device at relatively constant temperature in a vacuum comprising:
- 15 a substrate having first and second surfaces with an aperture formed therethrough,
said aperture being surrounded along with an area of said first surface by a contiguous wall structure, said area inside said wall being a bounded surface area,
20 a thermoelectric temperature stabilizer mounted firmly to said bounded surface so as to allow for fluid communication through said aperture, and having an opposite surface,
said infrared sensitive semiconductor device being mounted to the opposite surface of said thermoelectric temperature stabilizer,
25 a temperature sensor mounted to said infrared sensitive semiconductor device in thermal proximity to said sensor array,
a window for admitting some radiation to said infrared sensitive semiconductor device mounted to seal in said thermoelectric temperature stabilizer, temperature sensor and infrared sensitive semiconductor device in the interior space definable by said first surface, said window for admitting some radiation and said wall structure,
- 30
12. An apparatus as set forth in claim 9 wherein a sealable tube member is mounted to said second surface about said aperture.
- 35
13. A device as set forth in claim 12, wherein a getter is placed into said tube before sealing.

14. An apparatus as set forth in claim 10 wherein a sealable tube member is mounted to said second surface about said aperture.
- 5 15. A device as set forth in claim 14, wherein a getter is placed into said tube before sealing.
16. An apparatus as set forth in claim 11 wherein a sealable tube member is mounted to said second surface about said aperture.
- 10 17. A device as set forth in claim 16, wherein a getter is placed into said tube before sealing.
18. An apparatus as set forth in claim 12 wherein a sealable tube member is mounted to said second surface about said aperture.
- 15 19. A device as set forth in claim 18, wherein a getter is placed into said tube before sealing.
- 20 20. Method for reading out changes in resistivity of passive radiation receiving units in an n by m array including:
- exposing the radiation receiving surface to an irradiated scene desired to be observed, thus developing a change in resistivity of said receiving units related to the amount of radiation received from said scene,
- 25 sweeping the receiving units with a short duration pulse of bias current that heats the sensors and is too large to allow said units to remain stable if said pulse is of a substantially longer duration wherein said sweep is at such a rate that each said unit has time to return to a
- 30 stabilization temperature before the sweep generates a second short duration bias pulse to each said unit.
21. A method as set forth in claim 20 which is sensitive to changes in temperature induced by infrared radiation only.
22. A method as set forth in claim 20 which is sensitive to changes in temperature induced by infrared radiation only.
- 35 23. A method as set forth in claim 20 wherein said changes in temperature effect a change in resistivity of said radiation receiving surface.

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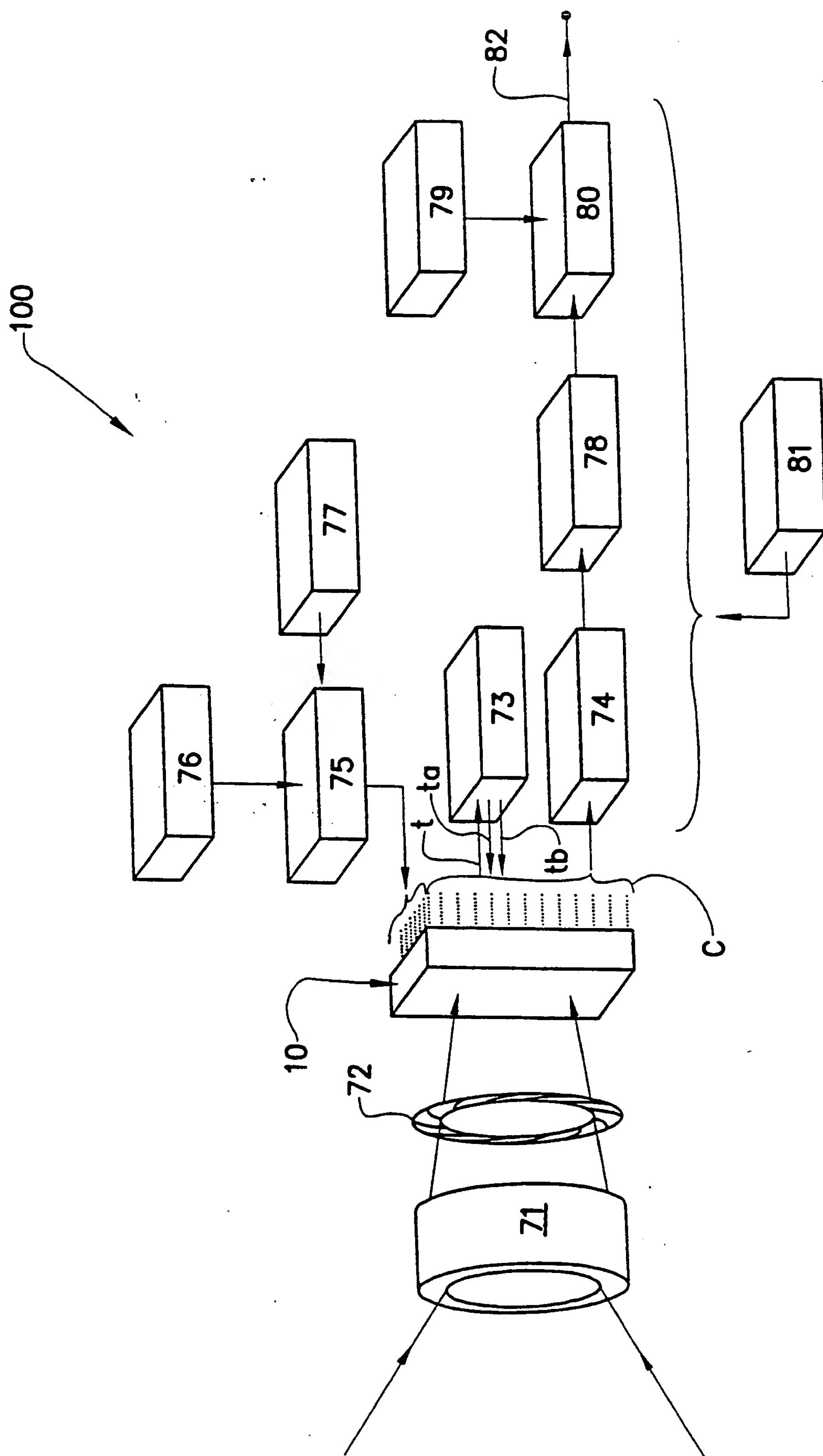
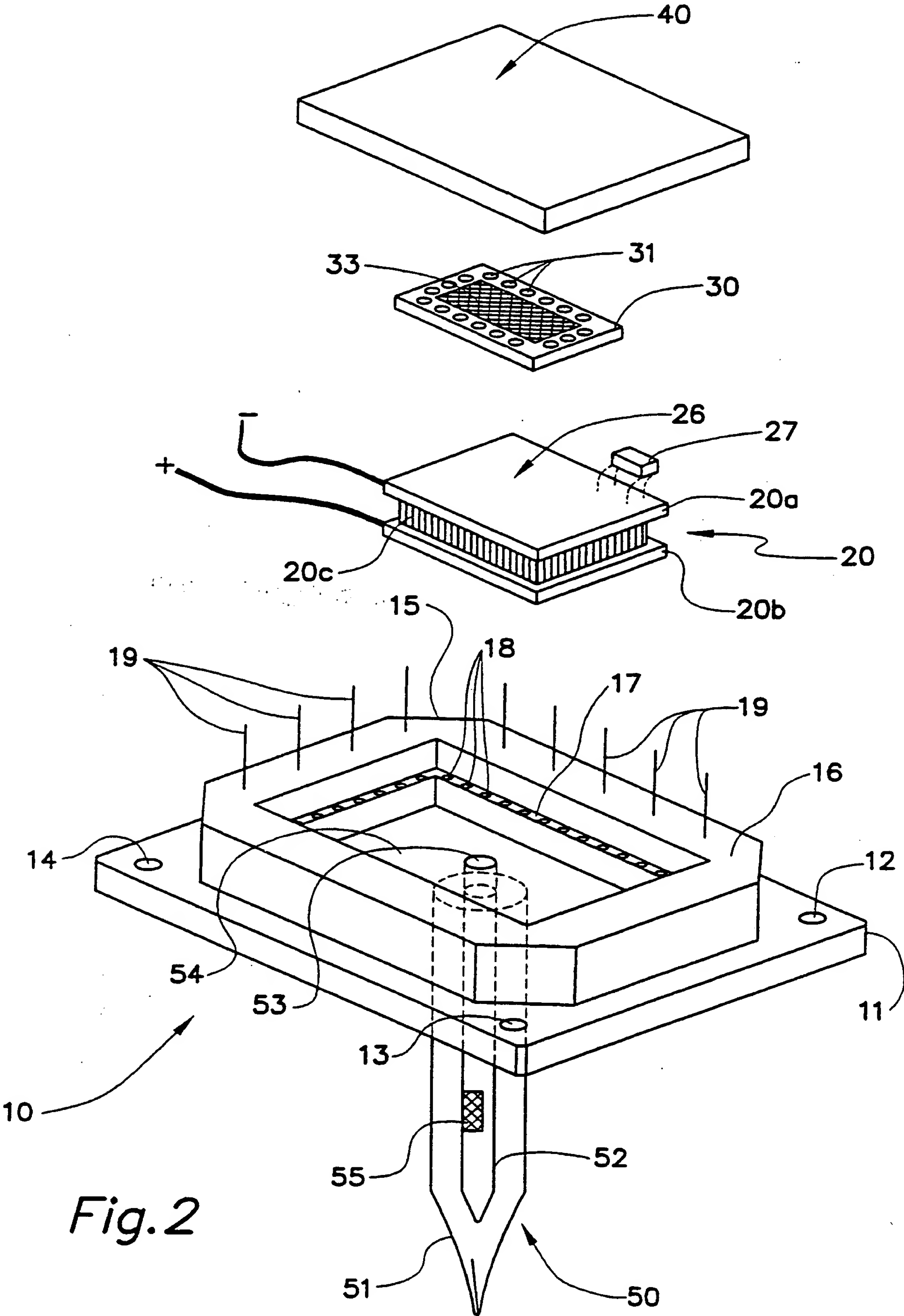


Fig. 1

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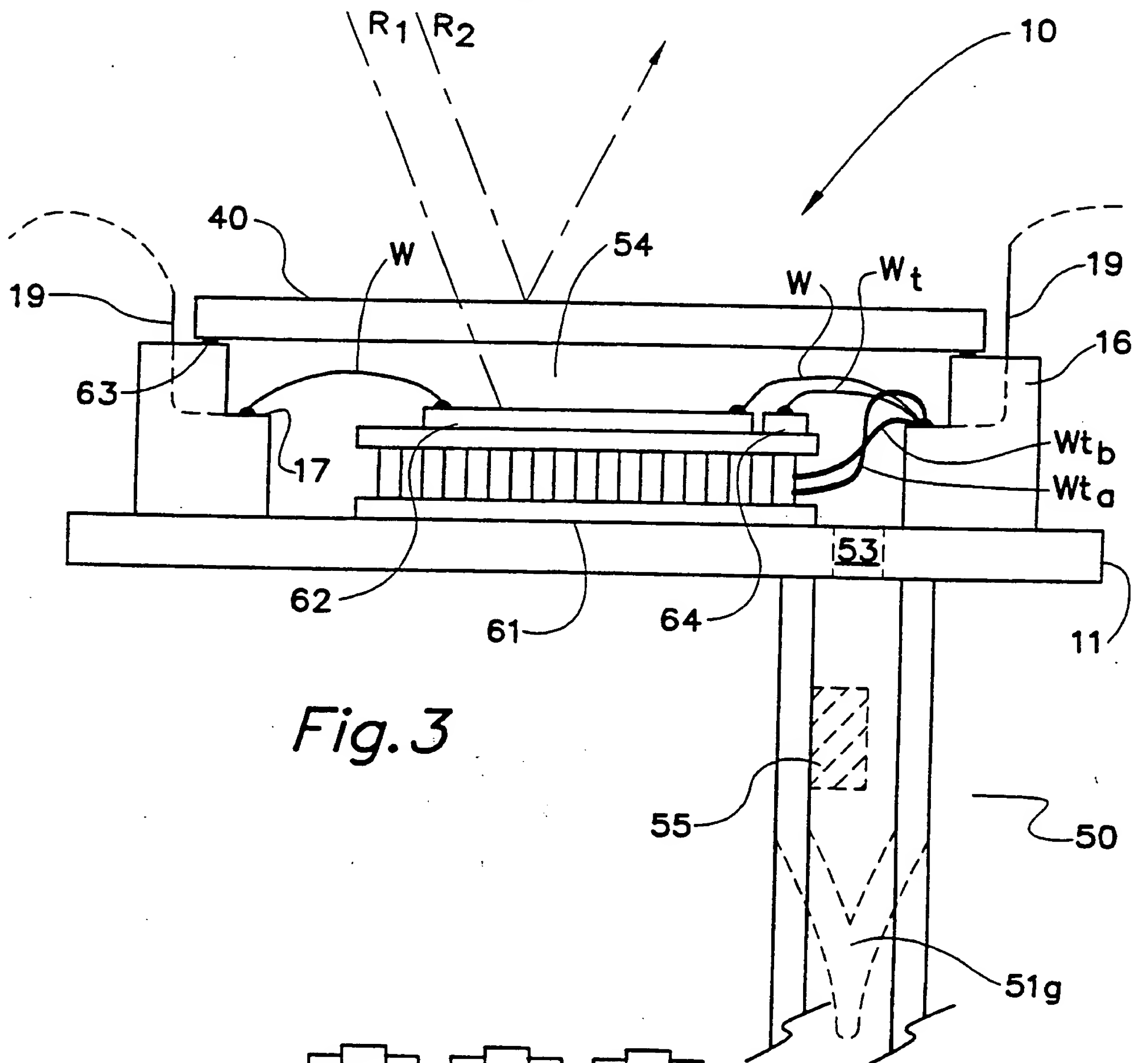


Fig. 3

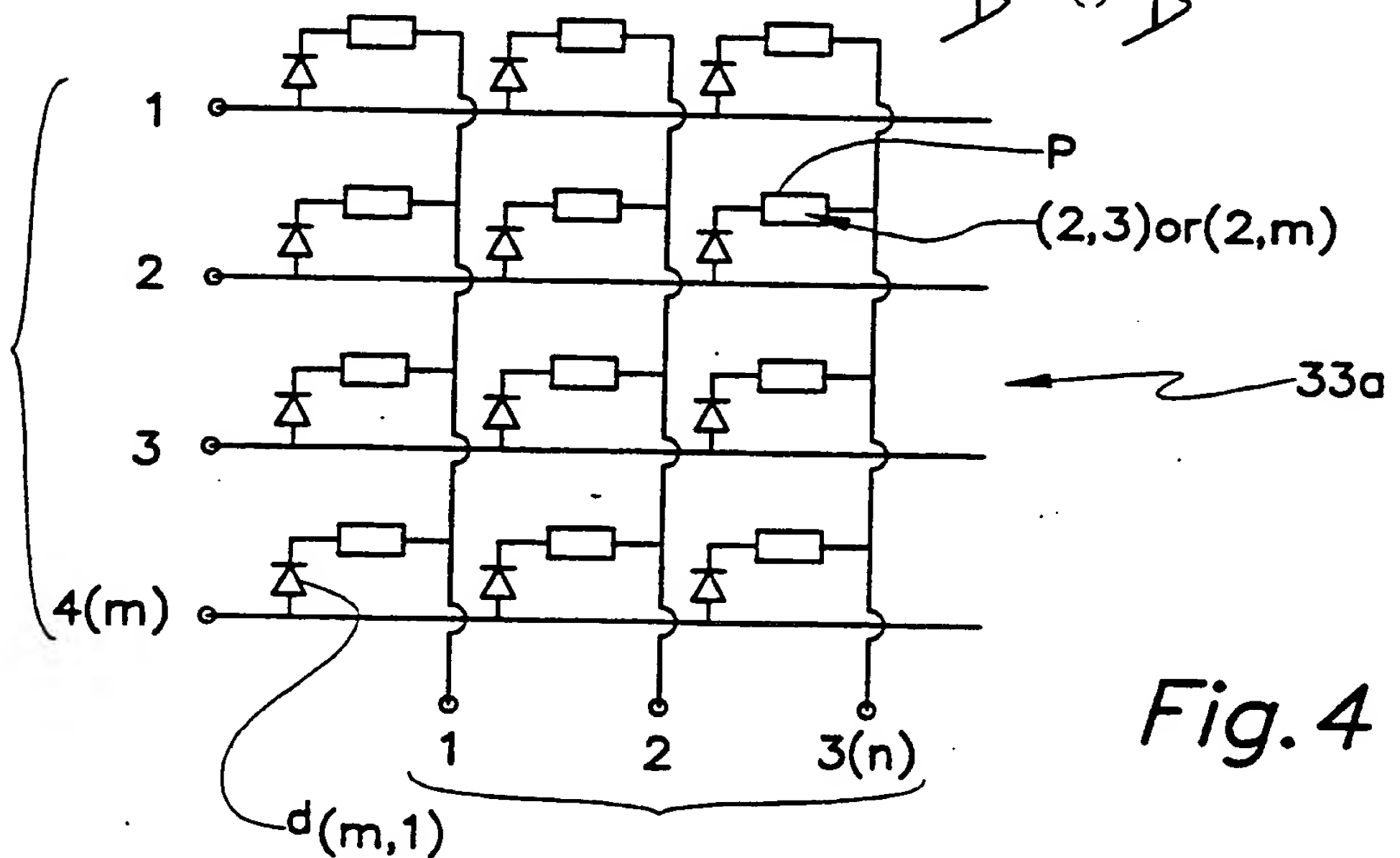


Fig. 4

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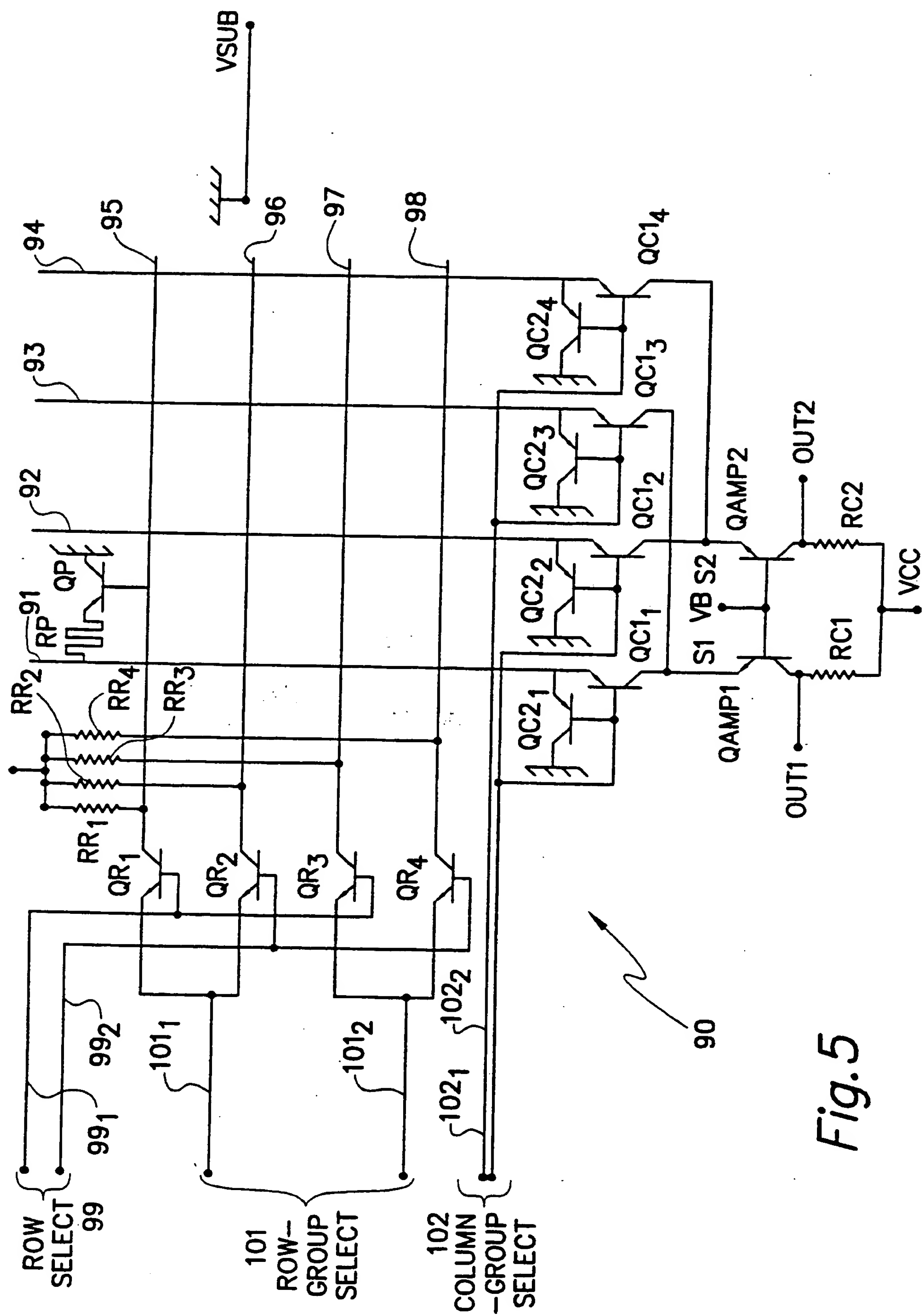


Fig. 5

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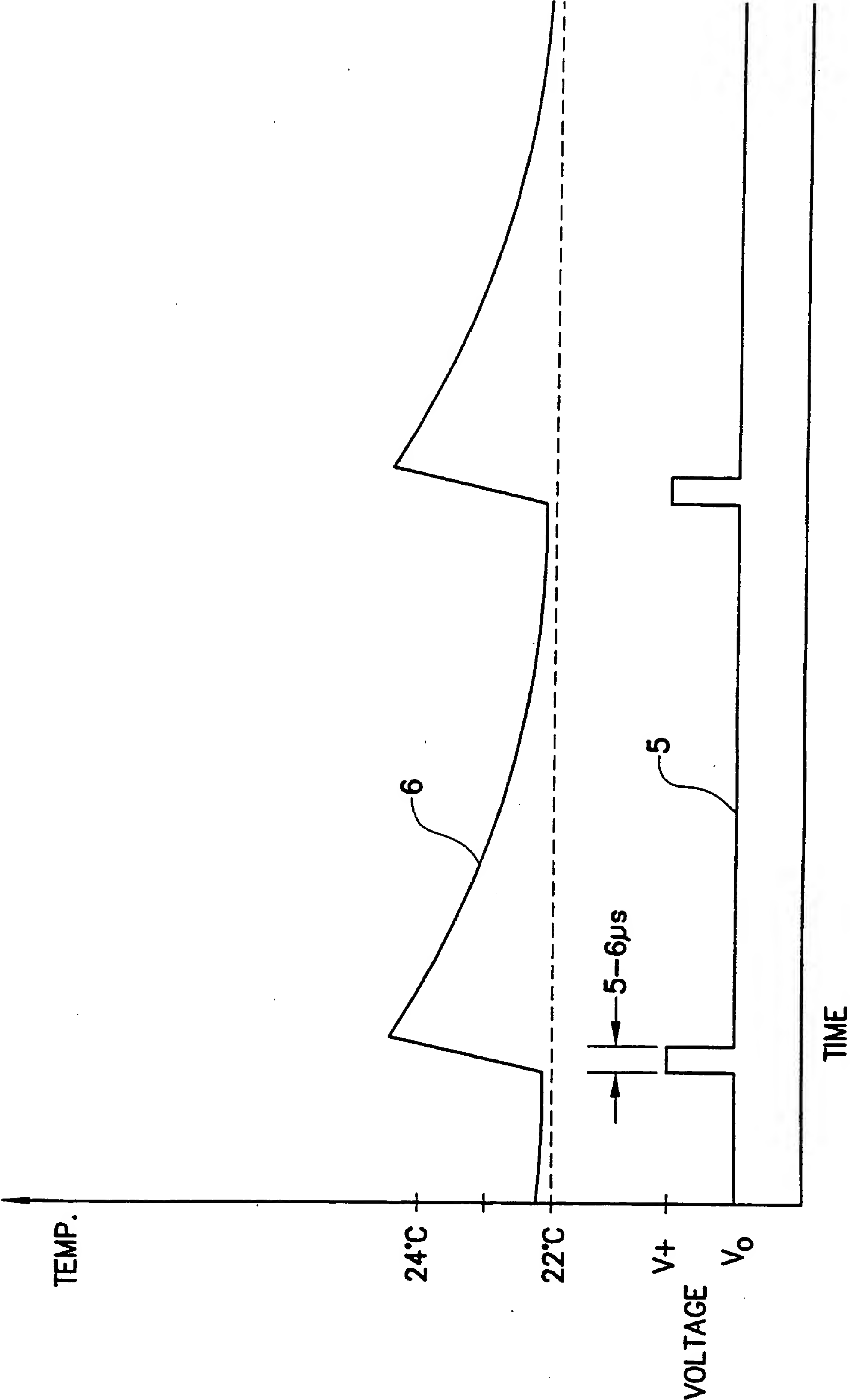


Fig. 6

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INTERNATIONAL SEARCH REPORT

International application No.
PCT/US 93/05740

A. CLASSIFICATION OF SUBJECT MATTER

IPC 5 H04N5/33 G01J5/06

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 5 H04N G01J H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US,A,4 752 694 (HEGEL, JR. ET AL.) 21 June 1988	1,2,4,6
A	see column 1, line 68 - column 2, line 51 ---	3,20-23
Y	WO,A,92 06561 (E.I. DU PONT DE NEMOURS AND COMPANY) 16 April 1992 see page 2, line 17 - page 3, line 17 see page 3, line 33 - page 6, line 18 ---	1,2,4,6
A	US,A,5 075 549 (PINES) 24 December 1991 see column 1, line 50 - column 2, line 4 see column 2, line 25 - column 3, line 60 ---	5-7
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Date of the actual completion of the international search

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/US 93/05740

C. (Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

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A	WO,A,91 16607 (COMMONWEALTH OF AUSTRALIA, THE SECRETARY DEPARTMENT OF DEFENCE) 31 October 1991 see page 10, line 1 - line 11 ---	1,9-11
A	PATENT ABSTRACTS OF JAPAN vol. 12, no. 234 (E-629) 5 July 1988 & JP,A,63 026 924 (OKAYA DENKI SANGYO) 4 February 1988 see abstract ---	12-19
A	RESEARCH DISCLOSURE no. 323, March 1991, EMSWORTH, GB page 217 XP000176316 'pulsed bolometer array' * the whole document * -----	20-23

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

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